

	1	2	3	4	5	6
A						A
B						B
C						C
D						D

Change List



File: Change List_sch.kicad_sch

Block Diagram



File: Block Diagram_sch.kicad_sch

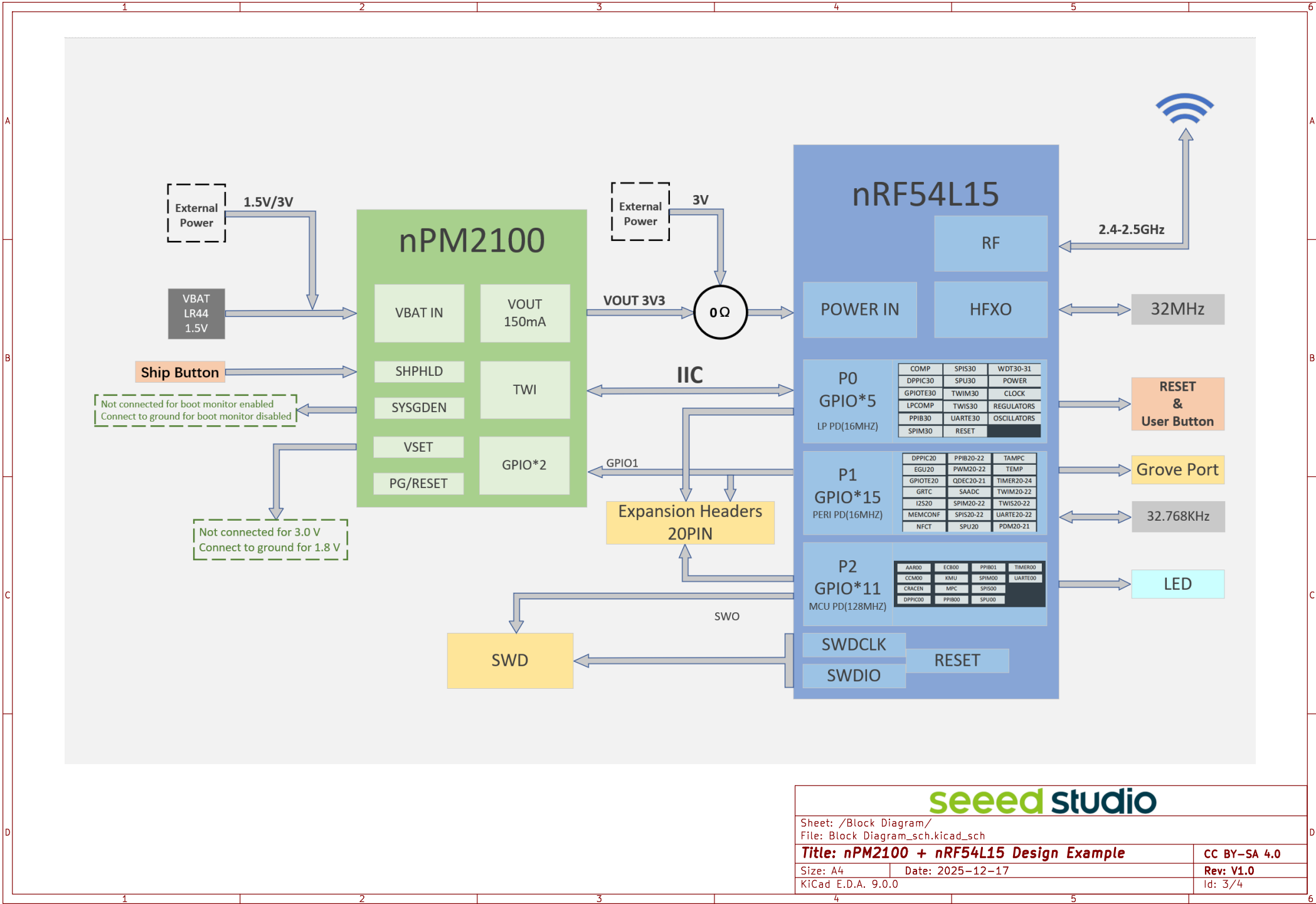
nPM2100+nRF54L15



File: nPM2100+nRF54L15_sch.kicad_sch

seeed studio		
Sheet: /		
File: nPM2100 + nRF54L15 Design Example_V1.0.kicad_sch		
Title: nPM2100 + nRF54L15 Design Example		CC BY-SA 4.0
Size: A4	Date: 2025-12-17	Rev: V1.0
KiCad E.D.A. 9.0.0		Id: 1/4

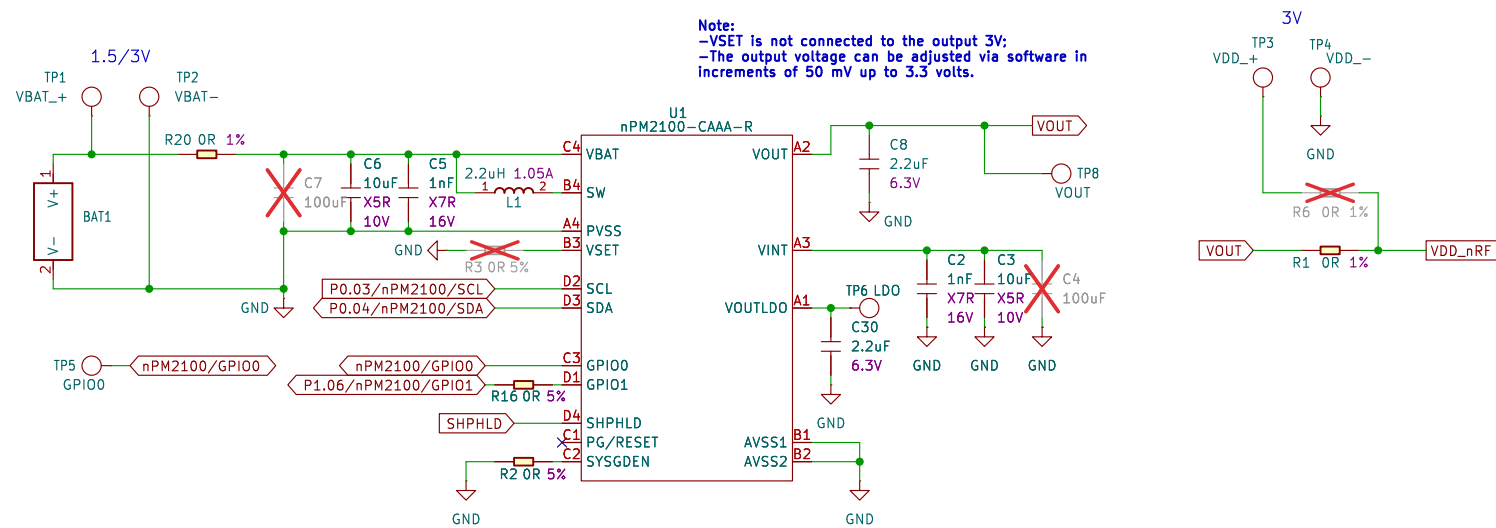
Version	Descriptions	Data	Name
V1.0	1.Initial	12/17/2025	



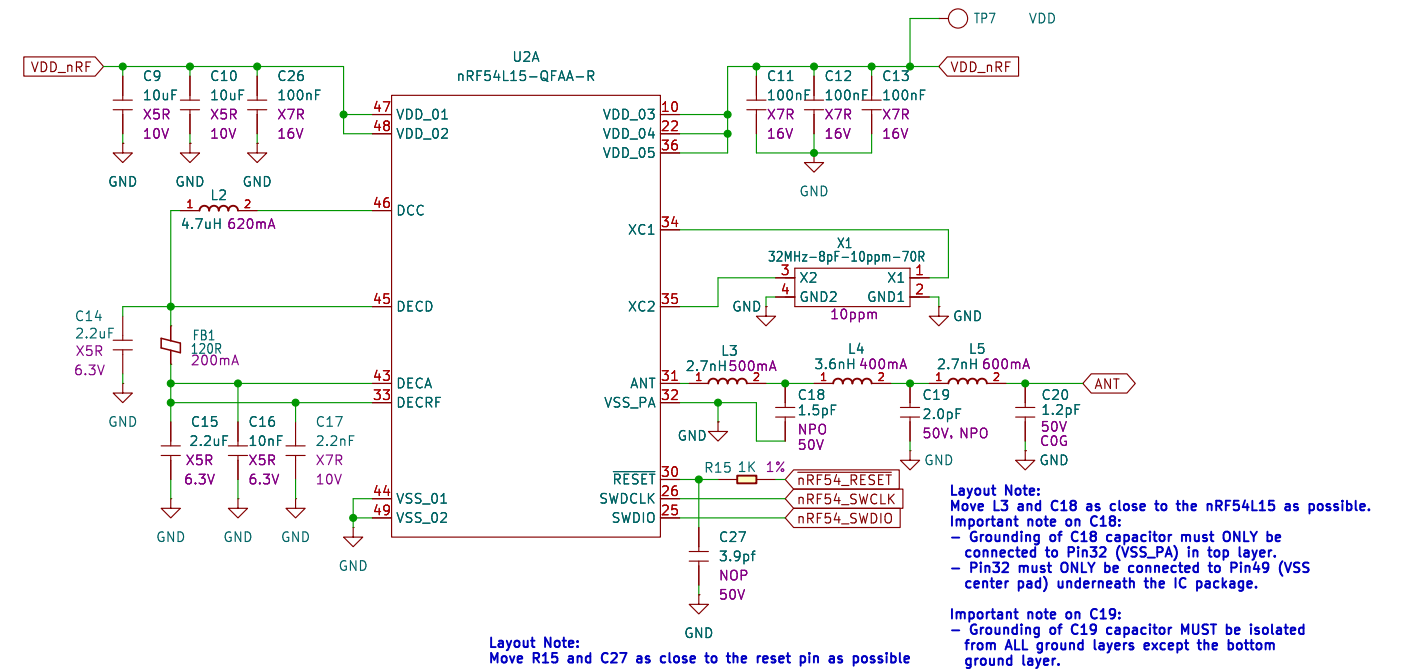
PMIC nPM2100

Value ID	Value	Description
		Output voltage setting (VOUT=1.8V+VLI*0.05V, legal range: 0-30)
1V8	0	1.8 V (default)
3V3	30	3.3 V

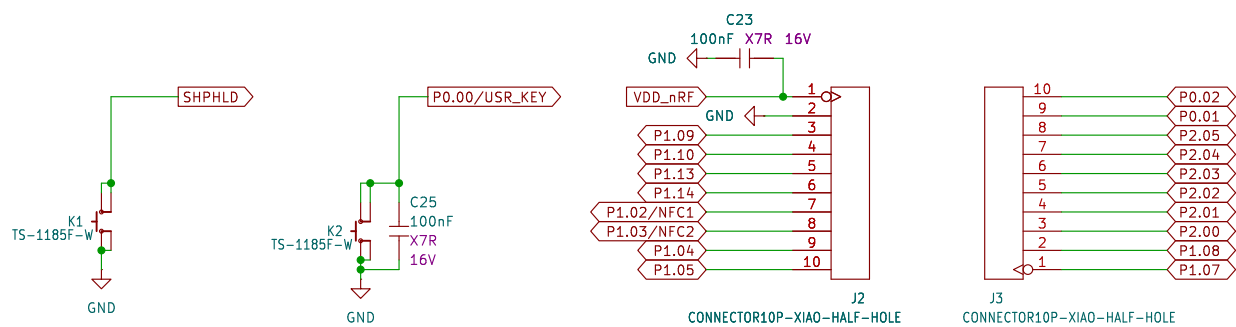
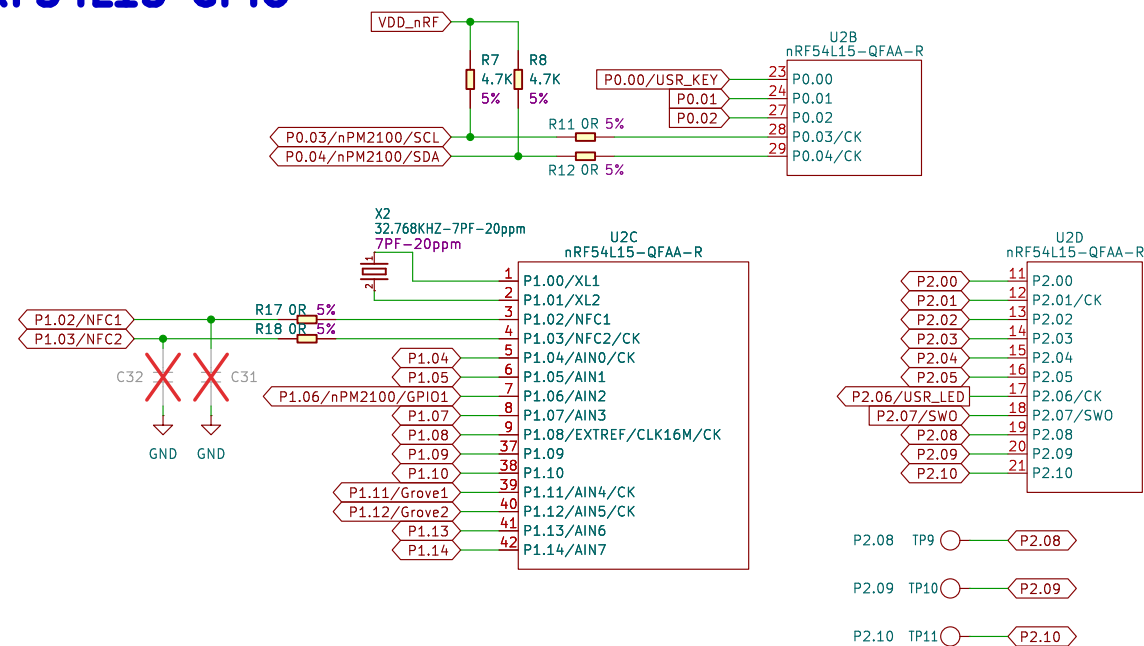
Note:
 -VSET is not connected to the output 3V;
 -The output voltage can be adjusted via software in increments of 50 mV up to 3.3 volts.



nRF54L15



nRF54L15 GPIO



Key

Header

Grove

SWD

LED